

### **AMENDMENTS TO THE DRAWINGS**

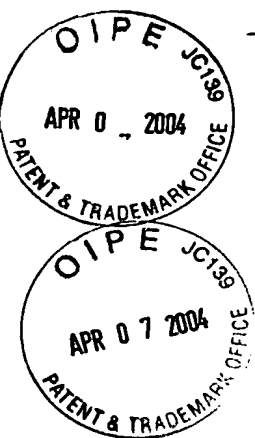
The attached sheets of drawings include changes to the drawings. These sheets, which include Figures 1-4, replace the original sheets including Figures 1-4.

In Figure 2, the clock generator unit 204 has been labeled as such. In addition, the reference number of the multiplier has been changed from "206" to "216".

In Figures 2-4, labels "CLK2" and "CLK" have been replaced with the label "second data clock signal", and the label "CLK1" that appeared in Figures 2-3 has been replaced with the label "first data clock signal".

In Figure 3, specific inputs to functional elements 304 and 306 are now shown, as described in the specification. In Figure 4, specific inputs to functional elements 412, 414 and 416 are now shown, as described in the specification.

Attachment: Replacement Sheets 1-4



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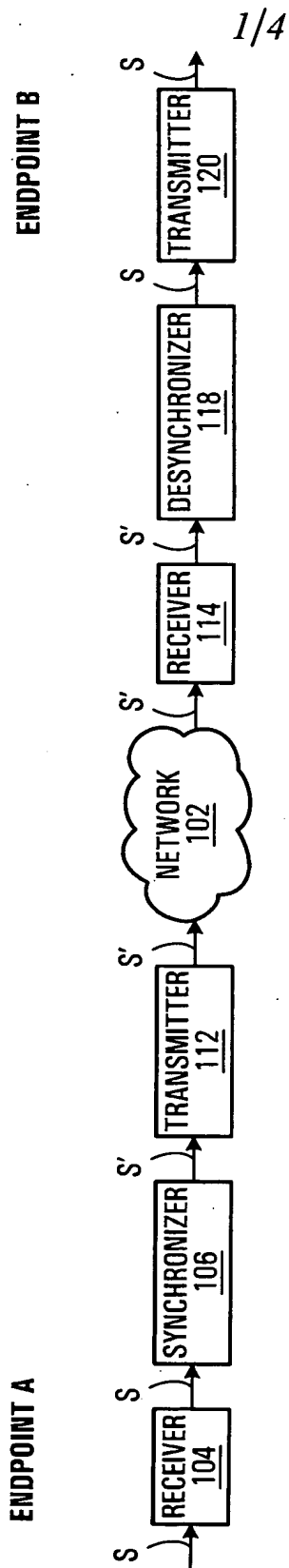


FIG. 1

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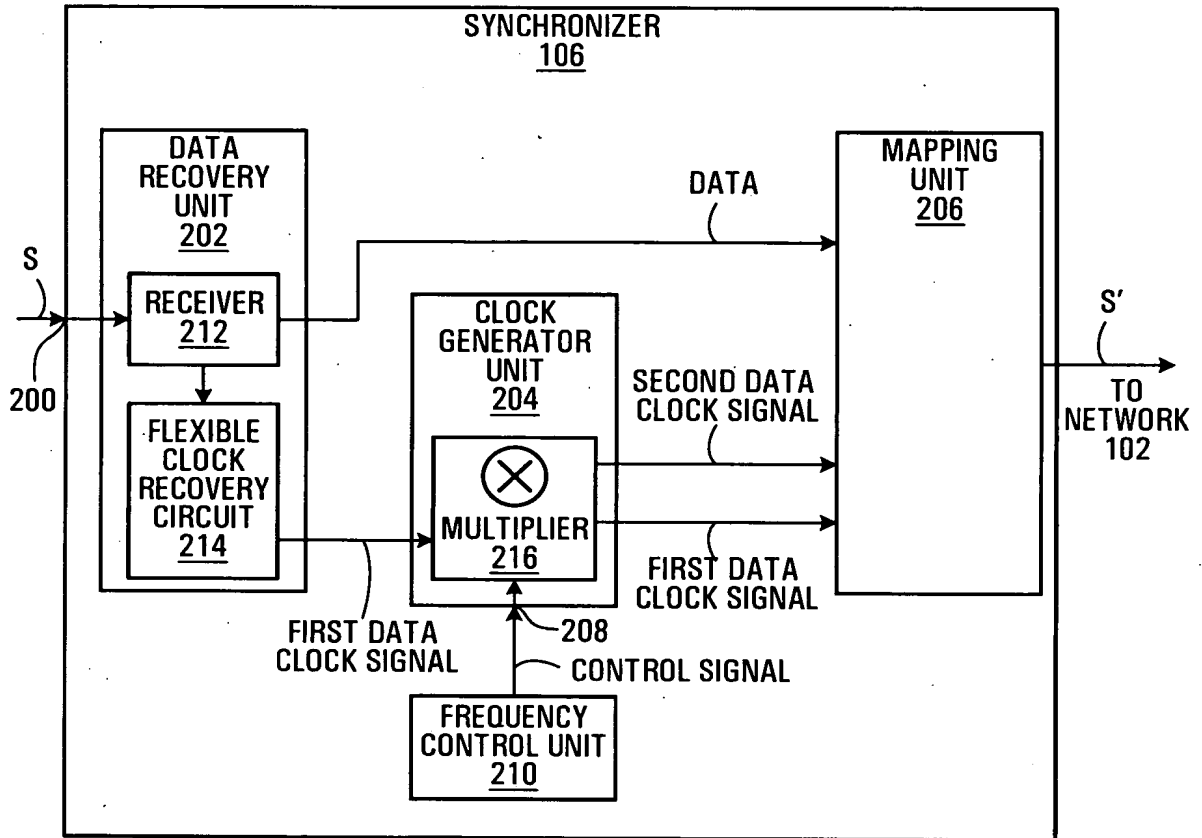
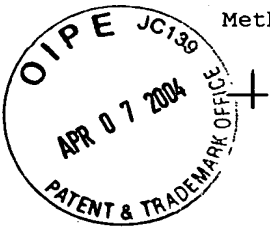


FIG. 2

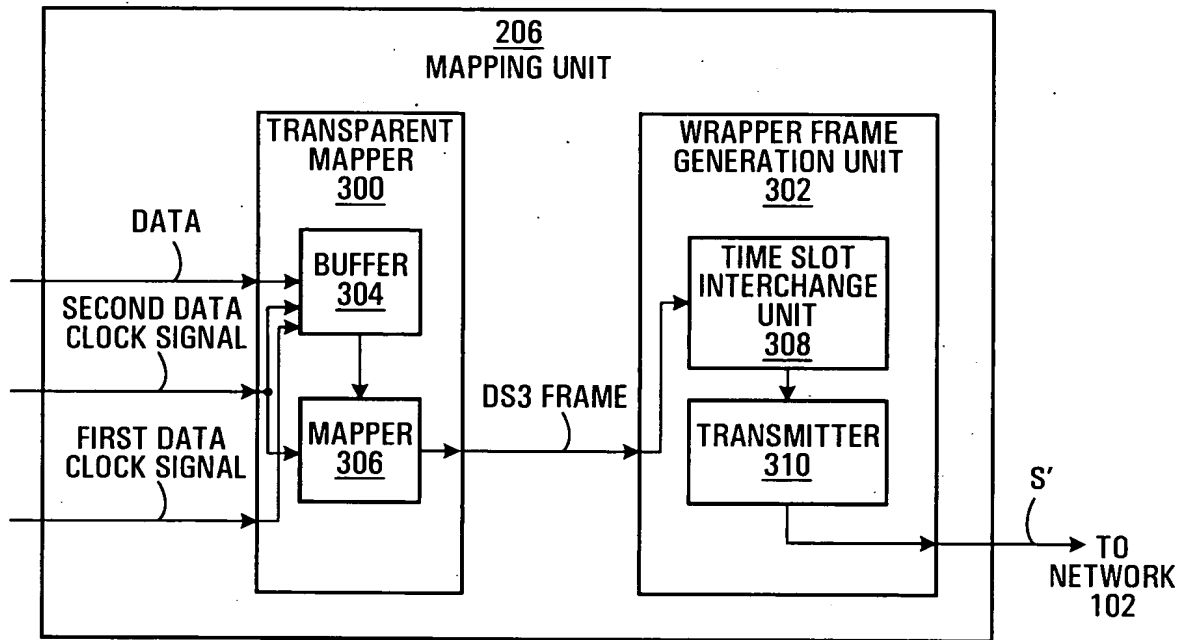
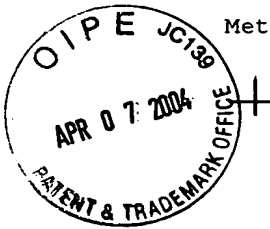


FIG. 3

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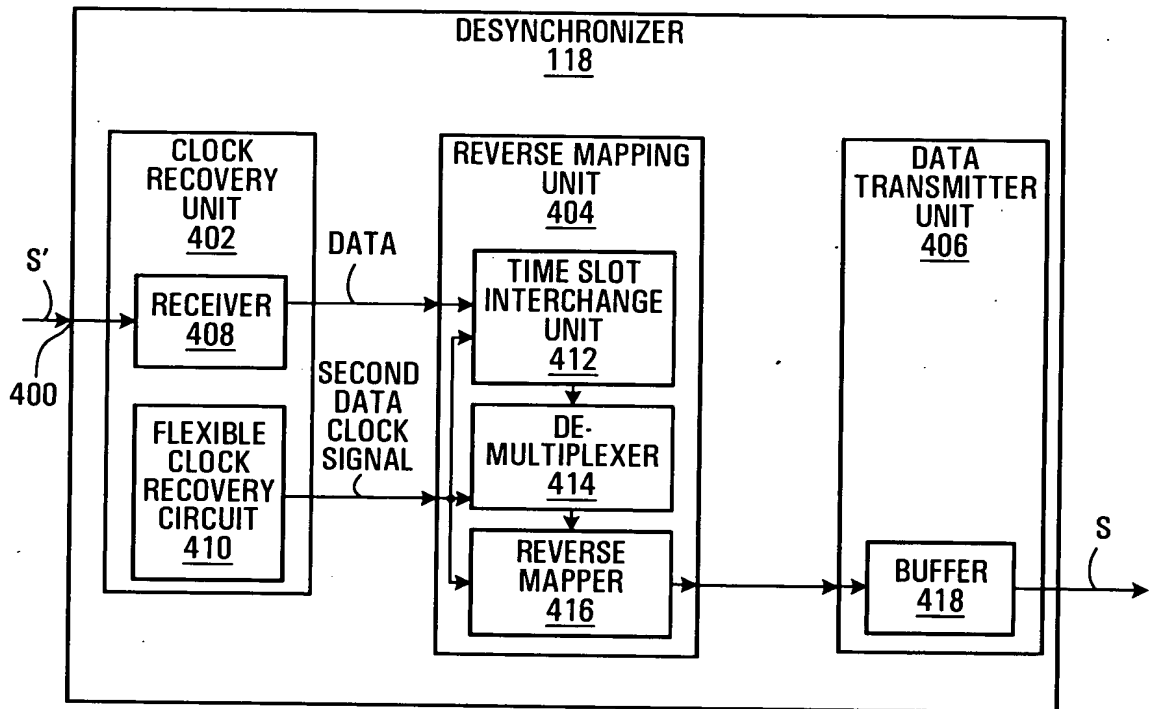


FIG. 4

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